

FIG.1

10
11
CHANNEL A
CHANNEL B
CHANNEL C
CHANNEL D
INITIALIZATION
INPUT A
INPUT B
INPUT C
INPUT D
OUTPUT
INITIALIZATION

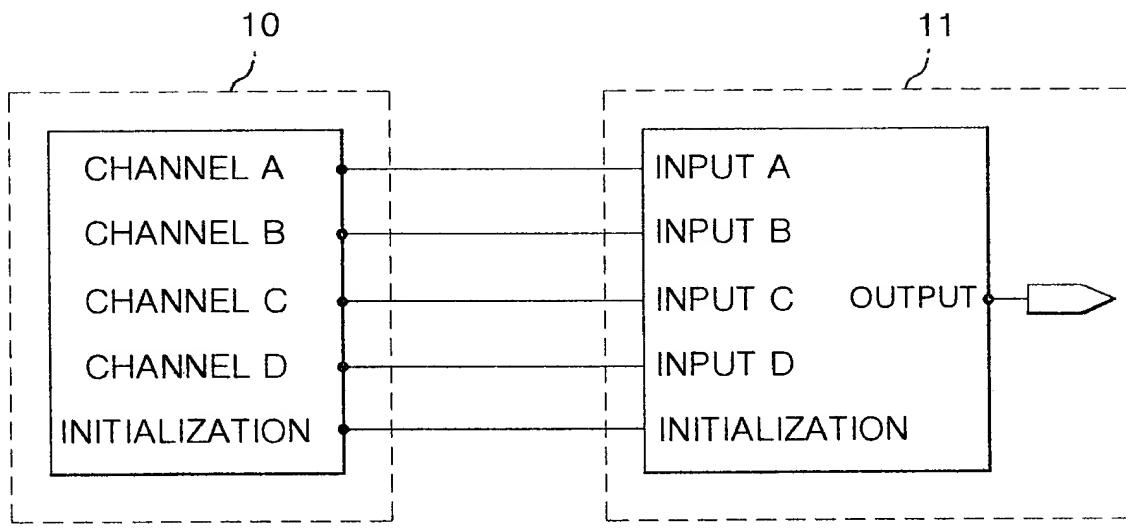


FIG.2

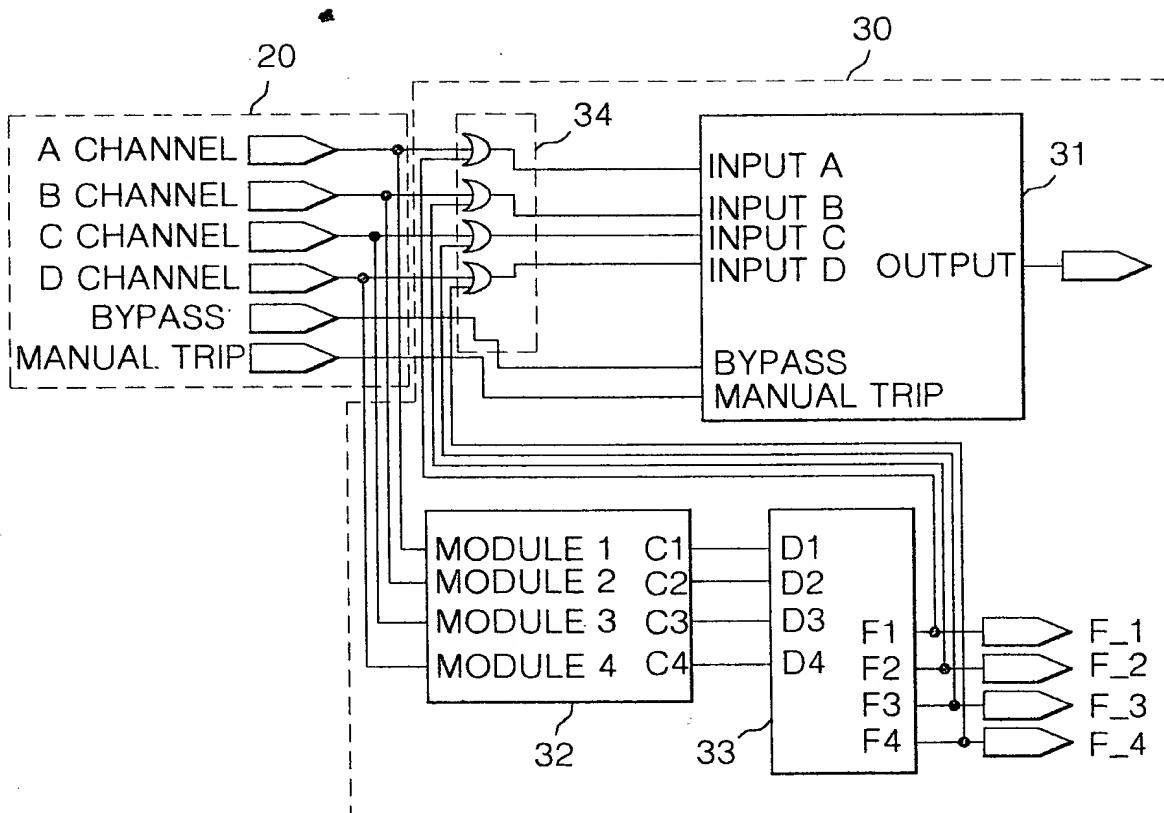


FIG.3

